Applicant: Dan M. White Attorney's Docket No.: Intel-017PUS Serial No.: 10/748,427 Intel Docket Number: P17944

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AMENDMENTS TO THE SPECIFICATION:

Please delete the paragraph beginning at page 9, line 8 and replace with the following paragraph:

FIG. 8 shows a schematic depiction of an exemplary architecture 800 600 for an embedded programming system having a debugger speedbar. A control module 802 602 controls the overall system functionality. A device interface module 804 604 communicates with the device for which an embedded program is being developed. An assembler module 806 606 assembles the source program in a convention manner and generates errors and warnings in a manner well known to one of ordinary skill in the art.

Please delete the paragraph beginning at page 9, line 15 and replace with the following paragraph:

A main module 808 608 generates a main screen, such as the main screen 200 of FIG. 2. A source module 810 610 generates a window for displaying program source code, such as the window 202 of FIG. 2. A psm module 812 612 generates a window for displaying hardware resources associated with the device, such as the window 204 of FIG. 4. And a speedbar module 814 614 generates a debugger speedbar, such as the speedbar 206 of FIG. 2.